

Fig.3

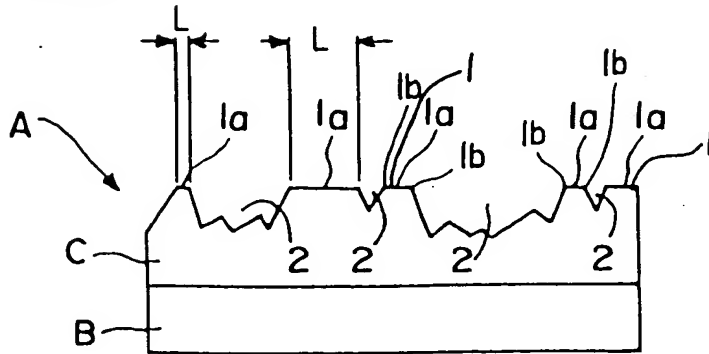
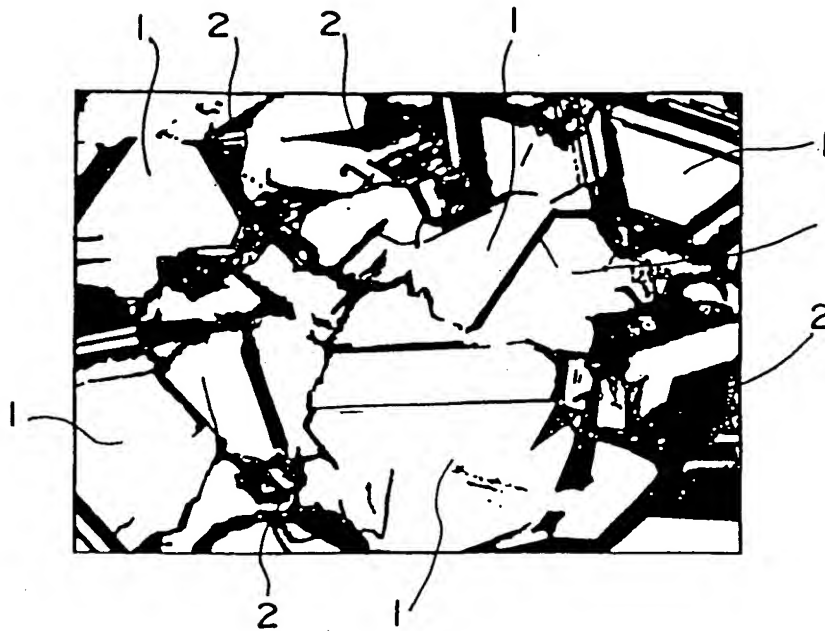


Fig.4



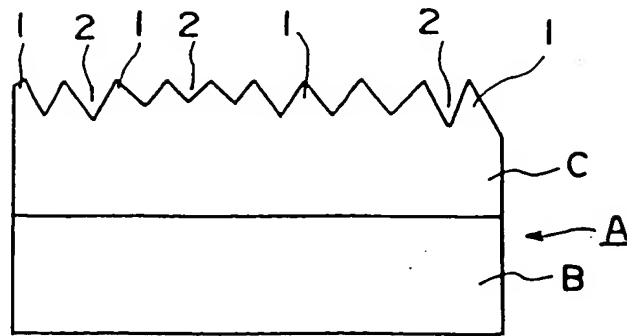
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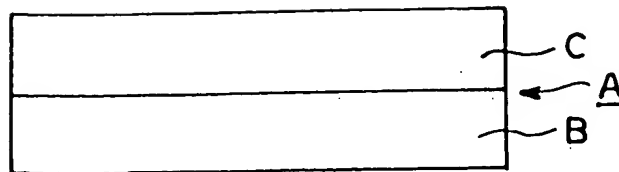
Title: SEMICONDUCTOR WAFER TREATMENT MEMBER  
Inventor(s): Masanari YOKOGAWA et al.  
Appl. No.: 10/603,781

**Fig.5**



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**Fig.6**



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